Docket No.: M4065.0133/P133-B (PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Brian A. Vaartstra, et al.

Application No.: Not Yet Assigned

Filed: Concurrently Herewith Art Unit: 2814

For: METHOD OF FORMING BARRIER

LAYERS (AS AMENDED)

Examiner: T. Le

Confirmation No.:

### **INFORMATION DISCLOSURE STATEMENT (IDS)**

MS Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

Those patent(s) or publication(s) which are marked with an asterisk (\*) in the attached form PTO/SB/08 (facsimile) are not supplied because they were previously cited by or submitted to the Office in a prior application no. 10/152,842 and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

Application No.: Not Yet Assigned Docket No.: M4065.0133/P133-B

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0133/P133-B. A duplicate copy of this paper is enclosed.

Dated: August 19, 2003

Respectfully submitted

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# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

Docket Number	Application Number		
M4065.133/P133-B	Not Yet Assigned		
Applicant(s)			
Brian Vaartstra and Don	ald Westmoreland		
Filing Date	Group Art Unit		

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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*	В	5,688,724		Yoon et al.			12/23/94
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#### Docket Number Application Number M4065.133/P133-B Not Yet Assigned INFORMATION DISCLOSURE CITATION Applicant(s) IN AN APPLICATION Brian Vaartstra and Donald Westmoreland Filing Date Group Art Unit **FOREIGN PATENT DOCUMENTS** DOCUMENT NO. COUNTRY REF DATE CLASS SUBCLASS Translations YES NO AA | EP 0 770 862 A1 | 02.05.1997 | Europe OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Matsuhashi et al., Optimum Electrode Materials for Ta<sub>2</sub>O<sub>3</sub> Capacitors at High and Low Temperature Processes, Extended Abstract of the 1993 International Conference on Solid State Devices and Materials, Makuhari, 1993, pp. 853-855. CC Kwon, et al., Degradation-Free Ta<sub>2</sub>O<sub>5</sub> Capacitor after BPSG Reflow at 850°C for High Density DRAMs, IEEE, 1993, pp. 53-56. Shappirio, et al., Diboride Diffusion Barriers in Silicon and GaAs Technology, J. Vac. DD Sci. Technol. B, Vol. 4, No. 6, Nov/Dec 1986, pp. 1409-1415. Rebhoiz, et al., Structure, Mechanical and Tribological Properties of Ti-B-N and Ti-Al-B-N Multiphase Thin Films Produced by Electron-Beam Evaporation", J. Vac. Sci. Technol. A 16(5), Sep/Oct 1998, pp. 2850-2857.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.